

OSC52

■用途

PC main board, VGA card, DVD,DVC,DSC,
Printer, Scanner, Game機

■特長

- Low height ceramic package, Size: 7.0×5.0×1.4 [mm]
- Low cost
- CMOS/TTL Output with Tri-state function



■仕様

Model	OSC52	
Nominal frequency range	0.500 ~ 200.000 MHz	0.500 ~ 107.000 MHz
Storage temperature range	-55~ +125°C	
Operating temperature range	0~+70°C	
Frequency stability	±20×10 ⁻⁶ , ±25×10 ⁻⁶ , ±50×10 ⁻⁶ , ±100×10 ⁻⁶	
Power supply voltage (Vdd)	+3.3V DC ±10%	+5.0V DC ±10%
Current consumption	10 mA max : 0.5~14.9MHz 15 mA max : 15~29.9MHz 20 mA max : 30~39.9MHz 25 mA max : 40~49.9MHz 30 mA max : 50~59.9MHz 35 mA max : 60~79.9MHz 45 mA max : 80~99.9MHz 50 mA max : 100~125MHz 65 mA max : 125.1~165MHz T.B.D. : 165.1~200MHz	10 (*20) mA max : 0.5~19.9MHz 10 (*35) mA max : 20~29.9MHz 15 (*35) mA max : 30~34.9MHz 30 (*35) mA max : 35~49.9MHz 30 (*60) mA max : 50~65.9MHz 50 (*60) mA max : 66~80.0MHz 60 mA max : 80.1~107MHz
Output level	TTL-CMOS (See Table A)	
Tri-state Function	#1:N.C. or "H"→Output enable / #1:"L"→Output disable (Hi-Z)	

Tabla A

Output level	CMOS	TTL (+5.0V only)
Load	15pF(*50pF)	2TTL, 5TTL, 10TTL
Level	V _{OL} :10%Vdd max. / V _{OH} :90%Vdd min.	V _{OL} :+0.4V max. / V _{OH} :+2.4V min.
Rise & Fall time	10ns max : F ≤ 40MHz, 7ns max : F > 40MHz, 5ns max : F > 100MHz at 10%~90% Vdd	at +0.4V - +2.4VDC
Duty cycle	45%-55% at 1/2Vdd F ≤ 50MHz 40%-60% at 1/2Vdd F > 50MHz	40%-60% at +1.4V

詳細仕様については、当社営業窓口にてご確認下さい。(Vdd:+1.8V,+2.5V,+2.8V+3.0V対応可)

■外形・寸法 [単位:mm]

Top view dimensions: 7.00±0.15 (width), 5.00±0.15 (height). Marking area is indicated.

Side view dimensions: 1.4±0.1 (height).

Terminal view dimensions: #1 offset 1.4±0.15, #2 offset 0.6±0.15, #3 offset 2.6±0.15, #4 offset 5.08±0.15.

Terminal	Connection
#1	OE/CE
#2	GND
#3	OUTPUT
#4	Vdd

Example of a Terminal Land Pattern

Land pattern dimensions: 2(x4) (width), 2.2 (height), 3.28 (pitch), 1.8(x4) (width).