

## OSC71

### ■用途

PC main board, VGA card, DVD,DVC,DSC,  
Printer, Scanner, Game機

### ■特長

- Ceramic package, Size: 5.0×3.2×0.9 [mm]
- 40~85°Cの広範囲な動作保証。
- Low cost
- CMOS/TTL output, with Tri-state function



### ■仕様

Model	OSC71	
Frequency range	0.500 ~ 156.250 MHz	0.500 ~ 110.000 MHz
Storage temperature range	-55 ~ +125°C	
Operating temperature range	0 ~ +70°C, -40 ~ +85°C	
Frequency stability	±25×10 <sup>-6</sup> , ±50×10 <sup>-6</sup> , ±100×10 <sup>-6</sup>	
Power supply voltage (Vdd)	+3.3V DC ±10%	+5.0V DC ±10%
Current consumption	10 (*10) mA max : 0.5~14.9MHz 15 (*15) mA max : 15~29.9MHz 20 (*25) mA max : 30~39.9MHz 25 (*30) mA max : 40~49.9MHz 30 mA max : 50~59.9MHz 40 mA max : 60~79.9MHz 50 mA max : 80~125MHz 55 mA max : 125.1~156.25MHz	10 (*15) mA max : 0.5~14.9MHz 15 (*20) mA max : 15~29.9MHz 25 (*30) mA max : 30~39.9MHz 35 (*40) mA max : 40~49.9MHz 40 mA max : 50~59.9MHz 45 mA max : 60~79.9MHz 50 mA max : 80~110MHz
Output level	TTL-CMOS (See Table A)	
Tri-state Function	#1:N.C. or "H"→Output enable / #1:"L"→Output disable (Hi-Z)	

Table A

Output level	CMOS	TTL (+5.0V only)
Load	15pF(*30pF)	2TTL, 5TTL, 10TTL
Level	V <sub>OL</sub> :10%V <sub>dd</sub> max. / V <sub>OH</sub> :90%V <sub>dd</sub> min.	V <sub>OL</sub> :+0.4V max. / V <sub>OH</sub> :+2.4V min.
Rise & Fall time	10ns max : F ≤ 40MHz, 7ns max : F > 40MHz, 2ns max : F > 125MHz	
	0.1V <sub>dd</sub> - 0.9V <sub>dd</sub>	+0.4V DC - +2.4V DC
Duty cycle	40%~60% at 1/2V <sub>dd</sub>	

詳細仕様については、当社営業窓口にてご確認ください。(V<sub>dd</sub>:+1.8V,+2.5V,+2.8V+3.0V対応可)

### ■外形・寸法 [単位:mm]

Example of a Terminal Land Pattern

Terminal	Connection
#1	OE/CE
#2	GND
#3	OUTPUT
#4	Vdd