

OSC73

■用途

Wireless-LAN, PLC modem, WiMax, Mobile communications, etc

■特長

- Ceramic package, Size: 5.0×3.2×1.05 [mm]
- High stability $\pm 10\text{ppm}/-40^{\circ}\text{C}\sim+85^{\circ}\text{C}$
- Low current consumption 3.1mA typ. (40MHz Vdd=+2.5V)
- Low phase noise, Low jitter
- CMOS output with Tri-state function



■仕様

Model	OSC73
Frequency range	4.000 ~ 54.000 MHz
Nominal Frequency [MHz]	5, 10, 12, 16, 20, 24, 32, 40, 44
Storage temperature range	-40 ~ +85°C
Operating temperature range	-40 ~ +85°C
Frequency stability	$\pm 10 \times 10^{-6}$, $\pm 15 \times 10^{-6}$ ※1
Power supply voltage (Vdd)	+2.5V, +3.0V, +3.3V DC $\pm 10\%$
Current consumption	7mA max. / 10uA max (Standby)
Output level	C-MOS
Load	15pF
Output voltage level	$V_{OL}: 10\%V_{dd}$ max. / $V_{OH}: 90\%V_{dd}$ min.
Rise & Fall time	5ns max. / 10%Vdd - 90%Vdd
Duty cycle	45%~55% at 1/2Vdd
Phase Noise / Jitter	-143dBc/Hz Typ. at 10kHz offset / 1 σ 3ps typ.
Tri-state Function	#1:N.C. or "H"→Output enable / #1:"L"→Output disable(Hi-Z)

特性向上の為、仕様を変更する場合があります。当社営業窓口にて最新の仕様をご確認ください。

※1 Frequency stability includes initial tolerance, temperature characteristics, supply voltage & load stability, reflow freq. shift, aging(1year @25°C).

■外形・寸法 [単位:mm]

Top view dimensions: 5.0 ± 0.2 mm width, 3.2 ± 0.2 mm height, MARKING area.

Side view dimensions: 1.05 ± 0.1 mm thickness.

Terminal view dimensions: #1 to #4 pins, 0.45 mm pin width, 1.6 mm pin pitch, 0.7 mm pin height, 2.6 mm distance between #1 and #3, 1.1 mm distance between #3 and #4.

Terminal	Connection
#1	CE
#2	GND
#3	OUTPUT
#4	Vdd

TEST端子 No Connection

Example of a Terminal Land Pattern